Form PTO-1449

INFORMATION DISCLOSURE CITATION IN AN APPLICATION

(Use several sheets if necessary)

Docket Number 188122000700	Application Number 10/773,541
Applicant: Yutao MA et al.	
Filing Date February 6, 2004	Group Art Unit 2825
Mailing Date June 17, 2004	

U.S. PATENT DOCUMENTS

Examiner Initials	Ref. No.	Date	Document No.	Name	Class	Subclass	Filing Date If Appropriate

FOREIGN PATENT DOCUMENTS

Examiner Initials	Ref. No.	Date	Document No.	Country	Class	Subclass	Translation YES	Translation NO

OTHER DOCUMENTS

(including author, title, Date, Pertinent Pages, Etc.)

Examiner Initials	Ref. No.	Title
TT	1.	Achar, R. et al., (May 2001). "Simulation of High-Speed Interconnects," *Proceeding of the IEEE* 89(5):693-728.
TT	2.	Bomhof, C. W. (2001). "Introduction," Chapter 1 *In* Interative and Parallel Methods for Linear Systems, With Applications in Circuit Simulation, located at <http://www.library.uu.nl/digiarchief/dip/diss/1957853/c1.pdf>, last visited on June 15, 2004, 15 pages (Includes Table of Contents).
TT	3.	Devgan, A. (1995) "Efficient and Accurate Transient Simulation in Charge-Voltage Plane," International Conference on Computer Aided Design (ICCAD '95) Proceedings of the 1995 IEEE/ACM international conference on Computer-aided design, San Jose, California, United States, Pages: 110 - 114.
TT	4.	Nguyen, T. V. et al., (1998). "Simulation of Coupling Capacitances Using Matrix Partitioning," International Conference on Computer Aided Design (ICCAD '98), Proceedings of the 1998 IEEE/ACM international conference on Computer-aided design, San Jose, California, United States Pages: 12 - 18.
TT	5.	Schwarz, D. E. et al., (2000). "Structural Analysis For Electric Circuits and Consequences for MNA," *Int. J. Circ. Theor. Appl.* 28:131-162.
TT	6.	Krummenacher, F. et al., (July 28, 2000). "RF EKV MOSFET Model Implementation," EPFL - Electronics Laboratories (LEG), EPF-Lausanne, Switzerland, Craft European Project No. 25710, July 28, 2000, WP2, Deliverable D2.1, Ecole Polytechnique Federale de Lausanne 4 pages.
TT	7.	Zhou, X. (2000). "Multi-Level Modeling of Deep-Submicron Mosfets and ULSI Circuits," *9th International Conference on Mixed Design of Integrated Circuits and Systems , Mixdes 2002,*

EXAMINER: Tuyen Tu — DATE CONSIDERED: 06/06/2006

EXAMINER: Initial if citation considered, whether or not the citation conforms with MPEP 609. Draw a line through the citation if not in conformance and not considered. Include a copy of this form with next communication to applicant.

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Mailing Date June ___, 2004	

		~~Wroclaw, Poland, June 20-22, 2002, 7 pages~~. (NOT CONSIDERED BECAUSE IT HAS NOT BEEN SUBMITTED)

EXAMINER: Tuyan Ton	DATE CONSIDERED: 06/06/2006

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